



FORM PTO-1449	SERIAL NO. 10/646,317	CASE NO. 13454US03
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE August 22, 2003	GROUP ART UNIT 2824
(use several sheets if necessary)		APPLICANT(S):

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
JHA	A1	5,170,375	12/1992	Mattausch et al.	365/230.03	
JHA	A2	5,752,264	05/1998	Blake et al.	711/144	
JHA	A3	5,781,498	07/1998	Suh	365/230.06	
JHA	A4	5,864,497	01/1999	Suh	365/72	
JHA	A5	6,040,999	03/2000	Hotta et al.	365/200	
JHA	A6	6,141,286	10/2000	Vo et al.	365/230.03	
JHA	A7	6,141,287	10/2000	Mattausch	365/230.03	
JHA	A8	6,144,604	11/2000	Haller et al.	365/221	
JHA	A9	6,154,413	11/2000	Longwell et al.	365/230.03	
JHA	A10	6,163,495	12/2000	Ford et al.	365/230.03	
JHA	A11	6,166,942	12/2000	Vo et al.	365/63	
JHA	A12	6,166,986	12/2000	Kim	365/230.06	
JHA	A13	6,166,989	12/2000	Hamamoto et al.	365/233	
JHA	A14	6,169,701	01/2001	Eto et al.	365/230.06	
JHA	A15	6,173,379	01/2001	Poplingher et al.	711/165	
JHA	A16	5,936,905	08/1999	Proebsting	365/208	
JHA	A17	6,147,893	11/2000	Liu	365/104	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
JHA	A18	KIYOO ITOH et al., "Trends in Low-Power RAM Circuit Technologies," Proceedings of the IEEE, April 1995, pp. 524-543, Vol. 83, No. 4, IEE
	A19	

EXAMINER <i>Jy H. H.</i>	DATE CONSIDERED 2/4/04
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.